


UTILITY PATENT APPLICATION TRANSMITTAL <small>(Only for new nonprovisional applications under 37 CFR 1.53(b))</small>	Attorney Docket No.	0039-7260-2S
	First Inventor or Application Identifier	Nobuo NAKAMURA, et al.
	Title	IMAGE PICKUP APPARATUS AND METHOD OF CONTROLLING THE IMAGE PICKUP APPARATUS

1525 U.S. PTO
 09/30/99
 06/29/99

APPLICATION ELEMENTS <small>See MPEP chapter 600 concerning utility patent application contents</small>	ADDRESS TO: Assistant Commissioner for Patents Box Patent Application Washington, DC 20231
Fee Transmittal Form (e.g. PTO/SB/17) (Submit an original and a duplicate for fee processing)	ACCOMPANYING APPLICATION PARTS
1. Specification Total Pages 40	6. <input checked="" type="checkbox"/> Assignment Papers (cover sheet & document(s))
2. Drawing(s) (35 U.S.C. 113) Total Sheets 9	7. <input type="checkbox"/> 37 C.F.R. §3.73(b) Statement (when there is an assignee) <input type="checkbox"/> Power of Attorney
3. Oath or Declaration Total Pages 2	8. <input type="checkbox"/> English Translation Document (if applicable)
a. <input checked="" type="checkbox"/> Newly executed (original)	9. <input type="checkbox"/> Information Disclosure Statement (IDS)/PTO-1449 <input type="checkbox"/> Copies of IDS Citations
b. <input type="checkbox"/> Copy from a prior application (37 C.F.R. §1.63(d)) (for continuation/divisional with box 15 completed)	10. <input type="checkbox"/> Preliminary Amendment
i. <input type="checkbox"/> DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b).	11. <input checked="" type="checkbox"/> White Advance Serial No. Postcard
4. <input type="checkbox"/> Incorporation By Reference (usable if box 4B is checked) The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4B, is considered to be part of the disclosure of the accompanying application and is hereby incorporated by reference therein.	12. <input type="checkbox"/> Small Entity Statement(s) <input type="checkbox"/> Statement filed in prior application. Status still proper and desired.
5. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below: <input type="checkbox"/> Continuation <input type="checkbox"/> Divisional <input type="checkbox"/> Continuation-in-part (CIP) of prior application no.: Prior application information: Examiner: Group Art Unit:	13. <input checked="" type="checkbox"/> Certified Copy of Priority Document(s) (1) (if foreign priority is claimed)
6. Amend the specification by inserting before the first line the sentence: <input type="checkbox"/> This application is a <input type="checkbox"/> Continuation <input type="checkbox"/> Division <input type="checkbox"/> Continuation-in-part (CIP) of application Serial No. Filed on	14. <input checked="" type="checkbox"/> Other: Notice of Priority
7. This application claims priority of provisional application Serial No. Filed	
17. CORRESPONDENCE ADDRESS OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C. FOURTH FLOOR 1755 JEFFERSON DAVIS HIGHWAY ARLINGTON, VIRGINIA 22202 (703) 413-3000 FACSIMILE: (703) 413-2220	

Name:	Marvin J. Spivak	Registration No.:	24,913
Signature:		Date:	6/29/99
Name:	C. Irvin McClelland	Registration No.:	

Registration Number 21,124

TITLE OF THE INVENTION

IMAGE PICKUP APPARATUS AND METHOD OF CONTROLLING THE
IMAGE PICKUP APPARATUS

BACKGROUND OF THE INVENTION

5 The present invention relates to a solid-state
image pickup apparatus, and more particularly to a
technique of increasing the dynamic range of a solid-
state image pickup apparatus of amplification type.

10 One of the important features of solid-state
image pickup apparatuses is dynamic range. The dynamic
range is determined by the signal charge that can be
accumulated in each unit cell. The smaller the unit
cell, the smaller the capacitance for accumulating the
15 signal charge, and the smaller the saturated signal
charge. If the saturated signal charge is small, it
will be impossible to increase the dynamic range.

20 No sufficient measures are taken to increase the
dynamic range, in conventional solid-state image pickup
apparatuses of amplification type. Consequently, the
conventional solid-state pickup apparatuses of this
type cannot have high performance.

BRIEF SUMMARY OF THE INVENTION

25 The object of the present invention is to provide
a solid-state image pickup apparatus the dynamic range
of which can be increased, and also to provide a method
of controlling the solid-state image pickup apparatus.

 According to the first aspect of the invention,

there is provided an image pickup apparatus which comprises: an array of unit cells arranged in rows and columns, each unit cell having a light-receiving device for receiving light and generating an electric charge
5 corresponding to the light, a charge-accumulating section for accumulating the electric charge generated by the light-receiving device, a transfer device for transferring the electric charge from the light-receiving device to the charge-accumulating section,
10 and a charge-limiting device for limiting the electric charge accumulated in the charge-accumulating section; a plurality of vertical signal lines extending along the columns of unit cells, respectively, each for receiving a electric data item corresponding to the
15 electric charge accumulated in the charge-accumulating section of any unit cell of the associated column; and a control circuit for controlling each of the unit cells, causing the charge-limiting device to limit the charge generated by the light-receiving device during a
20 first period and transferred to the charge-accumulating section through the transfer device and, adding, to the electric charge accumulated in the charge-accumulating section, the charge generated by the light-receiving device during a second period following the first
25 period and transferred to the charge-accumulating section through the transfer device.

In each unit cell, the charge in the

charge-accumulating section will be saturated if the light-receiving device receives a large amount of light. This may decrease the dynamic range of the image pickup apparatus. To prevent the dynamic range from decreasing, the charge generated by the light-receiving device during a period and transferred to the charge-accumulating section is limited (that is, a part of the charge is skimmed). The charge that the light-receiving device generates during a later period is added to the charge accumulated in the charge-accumulating section. The unit cell can therefore exhibit different photoelectric conversion characteristics during the first period and the second period, respectively. The first period may be longer than the second period, in which case the dynamic range can be increased.

In the first aspect of the invention, it is preferable that the control circuit control each of the unit cells of the same row such that the charge-limiting device limits the charge accumulated in the charge-accumulating section, during a horizontal blanking period and the charge transferred to the charge-accumulating section is added to the charge accumulated in the charge-accumulating section, during a different horizontal blanking period.

That is, the charges are limited and added in the unit cells of different rows during the same horizontal

blanking period (i.e., the period between two adjacent horizontal effective periods, during which signals are read from the cells to the vertical signal line). In other words, signal charges are limited in the unit
5 cells of one row, while signal charges are added in the unit cells of the another row.

Thus, in each unit cell, the signal charge is limited during a horizontal blanking period and signal charges are added during another horizontal blanking
10 period. As a result, the unit cell exhibits different photoelectric conversion characteristics during one-frame period. This makes it possible to increase the dynamic range of the image pickup apparatus.

In the first aspect of the invention, it is
15 alternatively desired that the control circuit controls the unit cells such that the charge-limiting device limits the charge to be accumulated in the charge-accumulating section and the charge transferred to the charge-accumulating section is added to the charge
20 accumulated in the charge-accumulating section, during the same vertical blanking period in all cell units.

That is, the charge is limited and the charges are added in each unit cell during the same vertical blanking period (i.e., the period between two adjacent
25 frame). Hence, not only the dynamic range of the image pickup apparatus can be increased, but also the signals can be read out with sufficient simultaneity.

In the first aspect of the invention, it is desired that each unit cell have a light-receiving device for receiving light and generating an electric charge corresponding to the light, a charge-accumulating section for accumulating the electric charge generated by the light-receiving device, a first transistor having a first control terminal and connected between the light-receiving device and the charge-accumulating section, for transferring the charge generated in the light-receiving device to the charge-accumulating section when a transfer signal is supplied to the first control terminal, and a second transistor having a second control terminal and connected to the charge-accumulating section, for limiting the charge accumulated in the charge-accumulating section when a voltage between off voltage and on voltage is applied to the second terminal.

According to the second aspect of the invention, there is provided a method of controlling an image pickup apparatus comprising: an array of unit cells arranged in rows and columns, each unit cell having a light-receiving device for receiving light and generating an electric charge corresponding to the light, a charge-accumulating section for accumulating the electric charge generated by the light-receiving device, a transfer device for transferring the electric charge from the light-receiving device to the

charge-accumulating section, and a charge-limiting device for limiting the electric charge accumulated in the charge-accumulating section; a plurality of vertical signal lines extending along the columns of unit cells, respectively, each for receiving a electric data item corresponding to the electric charge accumulated in the charge-accumulating section of any unit cell of the associated column. The method comprising the steps of: transferring the charge generated by the light-receiving device during a first period to the charge-accumulating section through the transfer device; limiting the charge transferred to the charge-accumulating section, by means of the charge-limiting device; and transferring the charge generated by the light-receiving device during a second period following the first period, to the charge-accumulating section through the transfer device, thereby adding the charge generated during the second period to the charge limited by the charge-limiting device.

According to the third aspect of the invention, there is provided an image pickup apparatus which comprises: an array of unit cells arranged in rows and columns, each having a light-receiving device for receiving light and generating an electric charge corresponding to the light; a plurality of vertical signal lines extending along the columns of unit cells, respectively, each for receiving a electric data item

corresponding to the electric charge generated by the light-receiving device of any unit cell of the associated column; a control circuit for controlling each of the unit cells of the same row such that
5 electric data items corresponding to electric charges generated by the light-receiving device during different periods, respectively, are read through the vertical signal line; and a data-holding circuit for temporarily holding the electric data items read by the
10 control circuit, so as to add the electric data items in an external circuit.

In this image pickup apparatus, electric data items corresponding to electric charges generated by the light-receiving device during different periods,
15 respectively, are read through the vertical signal line. Therefore, a composite photoelectric conversion characteristic can be obtained if the electric data items read are added by means of, for example, a signal-processing circuit. In this case, the unit
20 cells can have different photoelectric conversion characteristics merely by varying the charge-generating periods. The dynamic range of the image pickup apparatus can thereby be increased.

In the third aspect of the invention, it is
25 preferred that the control circuit controls each of the unit cells of the same row such that the electric data items are read from the unit cell through the vertical

signal line during different horizontal blanking periods, respectively. This means that electric data items are read from the unit cells of the different rows during the same horizontal blanking period.

5 Since the electric data items are read from each unit cell through the vertical signal line during different horizontal blanking periods, the unit cell can exhibit different photoelectric conversion characteristics during one-frame period. This makes it
10 possible to increase the dynamic range of the image pickup apparatus.

 According to the fourth aspect of the invention, there is provided an image pickup apparatus which comprises: an array of unit cells arranged in rows and
15 columns, each having a light-receiving device for receiving light and generating an electric charge corresponding to the light; a plurality of vertical signal lines extending along the columns of unit cells, respectively, each for receiving a electric data item
20 corresponding to the electric charge generated by the light-receiving device of any unit cell of the associated column; a vertical control circuit for controlling the unit cells such that electric data
25 items generated by the unit cells of different rows are read to the vertical signal lines during the same horizontal blanking period, and that electric data items corresponding to electric charges generated by

the light-receiving device of each unit cell of the same row during different periods, respectively, are read through the vertical signal lines during different horizontal blanking periods; a plurality of data-
5 holding circuits for temporarily holding the electric data items read from the unit cells of different rows during the same horizontal blanking period under the control of the vertical control circuit; a horizontal control circuit for controlling reading of the electric
10 data items from the data-holding circuits; a storage circuit for storing the electric data items read from the data-holding circuits under the control of the horizontal control circuit; and an adder circuit for adding those electric data items stored in the storage
15 circuit, which correspond to the charges generated by the same light-receiving device during different periods.

Additional objects and advantages of the invention will be set forth in the description which follows, and
20 in part will be obvious from the description, or may be learned by practice of the invention. The objects and advantages of the invention may be realized and obtained by means of the instrumentalities and combinations particularly pointed out hereinafter.

25 BRIEF DESCRIPTION OF THE SEVERAL VIEWS OF THE DRAWING

The accompanying drawings, which are incorporated in and constitute a part of the specification,

illustrate presently preferred embodiments of the invention, and together with the general description given above and the detailed description of the preferred embodiments given below, serve to explain the principles of the invention.

FIG. 1 is a block diagram showing the basic structure of a solid-state image pickup apparatus of amplification type, which is the first embodiment of the invention;

FIG. 2 is an equivalent circuit diagram of one of the unit cells incorporated in the image pickup apparatus shown in FIG. 1;

FIG. 3 is a sectional view showing one of the unit cells incorporated in the image pickup apparatus shown in FIG. 1;

FIGS. 4A to 4F are diagrams explaining the sequence of operation of the image pickup apparatus shown in FIG. 1;

FIG. 5 is a timing chart illustrating how the image pickup apparatus of FIG. 1 operates, mainly during the horizontal period;

FIG. 6 is a timing chart illustrating how the image pickup apparatus of FIG. 1 operates, mainly during the vertical period;

FIG. 7 is a timing chart explaining how the second embodiment of this invention operates;

FIG. 8 is also a timing chart explaining how a

modification of the second embodiment operates;

FIG. 9 is a timing chart showing how the third embodiment operates, mainly during the horizontal period;

5 FIG. 10 is a timing chart illustrating how a modification of the third embodiment operates, mainly during the horizontal period;

FIG. 11 is a timing chart explaining how the third embodiment operates, mainly during the vertical period;

10 FIG. 12 is a timing chart explaining how a modification of the third embodiment operates, mainly during the vertical period;

FIG. 13 is a block diagram depicting the basic structure of a solid-state image pickup apparatus of amplification type, which is the third embodiment of the invention;

15 FIG. 14 is a graph representing the relation between the amount of light input to the third embodiment and the output generated by the sensor provided in the third embodiment;

20 FIG. 15 is a graph showing the relation between the amount of light input to the third embodiment and the output generated by the signal-processing circuit incorporated in the third embodiment;

25 FIG. 16 is a sectional view showing mainly one of the unit cells incorporated in an image pickup apparatus which is the fourth embodiment of the present

invention;

FIG. 17 is a timing chart explaining how the fourth embodiment operates, mainly during the vertical period;

5 FIG. 18 is a timing chart explaining how the fourth embodiment operates, mainly during the vertical period; and

10 FIG. 19 is a timing chart explaining how another modification of the fourth embodiment operates, mainly during the vertical period.

DETAILED DESCRIPTION OF THE INVENTION

Embodiments of the present invention will be described, with reference to the accompanying drawings.
(First Embodiment)

15 FIG. 1 is a block diagram showing the main section of a typical solid-state image pickup apparatus of amplification type, which is the first embodiment of the invention.

20 As FIG. 1 shows, the image pickup apparatus comprises an image sensor 11, a line memory 12, a vertical register 13, a horizontal register 14, a timing generator 15, and an on-chip amplifier 16. The components 11 to 16 are provided on the semiconductor substrate 10.

25 The image sensor 11 has a plurality of unit cells (not shown) and a plurality of vertical signal lines (not shown). The unit cells are arranged in rows and

columns. The vertical signal lines extend along the columns of unit cells. Each unit cell accumulates signal charge representing data (electrical information). The data is read to a vertical signal line when the vertical register 13 outputs a control signal to the unit cell, in response to a timing signal supplied from the timing generator 15. The data, thus read, is stored into the line memory 12. The horizontal register 14 is driven, thereby reading the data (i.e., an electric signal) from the line memory 12 and outputting the same from the image pickup apparatus via the on-chip amplifier 16.

FIG. 2 is an equivalent circuit diagram of one of the unit cells. As shown in FIG. 2, the unit cell comprises a photodiode 21, a transfer (reading) transistor 22, an amplifying transistor 23, an address transistor 24, a reset transistor 25, and a detection node 26. The transfer transistor 22, address transistor 24 and reset transistor 25 are respectively controlled by control signals Φ_{read} , Φ_{addr} and Φ_{reset} which have been supplied from the vertical register 13 (FIG. 1).

The basic operation of the unit cell shown in FIG. 2 will be described below.

When light is applied through a lens (not shown) to the photodiode 21, the photodiode 21 generates a signal charge Q_{sig} (either electron charge or hole charge)

from the light. (That is, the photodiode 21 performs photoelectric conversion.) When the control signal Φ_{read} is supplied to the transfer transistor 22, the transistor transfers the signal charge Q_{sig} to the detection node 26. The signal charge Q_{sig} is accumulated in the parasitic capacitance 26a of the detection node 26.

The signal charge Q_{sig} in the parasitic capacitance 26a is converted to a voltage signal V_{sig} . Assume that the capacitance C of the detection node 26 is constant. Then, $V_{sig} = Q_{sig}/C$. The signal V_{sig} is read through the amplifying transistor 23 to the vertical signal line 28 when the address signal Φ_{addr} turns on the address transistor 24.

FIG. 3 is a sectional view of the unit cell region of the solid-state image pickup apparatus (FIG. 1) and illustrates one of the unit cells.

The unit cell comprises a photodiode 31, a transfer transistor 32 (i.e., transfer gate), a detection node 33, a reset transistor 34 (i.e., reset gate), and a power supply section 35. (The photodiode 31 has a p-type region in the surface of a charge-accumulating region of n type.) The components 31 to 35 are provided on a P-type silicon substrate 30. (The substrate 30 may be replaced by an N-type silicon substrate having a p-type well.) The unit cell is electrically isolated from any other unit cell by a

LOCOS element-isolating region 36. A capacitor may be connected in parallel to the parasitic capacitance of the detection node 33.

5 The photodiode 31, transfer transistor 32,
detection node 33 and reset transistor 34 are
equivalent to the photodiode 21, transfer transistor 22,
detection node 26 and reset transistor 25, all shown in
FIG. 2, respectively. The transfer transistor 32 and
10 the reset transistor 34 are controlled, also by the
control signals Φ_{read} and Φ_{reset} (FIG. 2), both
supplied from the vertical register 13 (FIG. 1).

How the image pickup apparatus, or the first
embodiment of the invention, operates will be described
with reference to FIGS. 4A to 4F. FIGS. 4A to 4F show
15 the various potentials, which the components of the
unit cell shown in FIG. 3 have while the image pickup
apparatus is operating.

The element-isolating region 36 has a potential a
little lower than 0V, due to the p-type region provided
20 below the region 36. Generally, the photodiode 31 has
a potential of about 1V. Nonetheless, it may have a
potential of 1V or more, as np-type photodiodes.

As shown in FIG. 4A, the photodiode 31 accumulates
signal charge Q1 and has the potential shown. In other
25 words, the photodiode 31 holds the signal charge built
in it in a predetermined period, because the transfer
transistor 32 remains off. This accumulation of signal

charge will be referred to as "first accumulation." At this time, the reset transistor 34 is on, whereby the detection node 33 is reset at the same potential as the power supply section 35. (The power supply voltage is 5 3.3V in the image pickup apparatus shown FIG. 1.)

Then, the reset transistor 34 is turned off, and the transfer transistor 32 is turned on, as is illustrated in FIG. 4B. As a result, the potential of the transfer transistor 32 increases, and the signal 10 charge Q1 is applied from the photodiode 31 to the detection node 33. Since the signal charge Q1 is applied to the node 33 in its entirety, the potential of the photodiode 31 increases in accordance with the signal charge Q1.

15 Next, as shown in FIG. 4C, a prescribed voltage Vref is applied to the reset transistor 34. The voltage Vref has a value between the on-voltage and off-voltage of the reset transistor 34. Therefore, a part of the signal charge Q1 is skimmed from the 20 detection node 33. As a result, signal charge Q2 is left in the detection node 33. Thus, the reset transistor 34 sets a limit to the charge accumulated in the detection node 33. The voltage Vref may be applied to the reset transistor 33 in two alternative methods. 25 The first method is to apply the voltage Vref in the form of a pulse voltage. The second is to apply the voltage Vref in the form of a DC voltage. The first

method is more desirable in view of the reliability of the gate insulating film of the reset transistor 34.

Further, as shown in FIG. 4D, signal charge is accumulated in the photodiode 31 for the second time, for a time shorter than the time of the first accumulation. As a result, the photodiode 31 accumulates signal charge Q3.

Then, the transfer transistor 32 is turned on as shown in FIG. 4E. The signal charge Q3 is thereby transferred to the detection node 33. The signal charge Q3 is added to the signal charge Q2 remaining in the detection node 33.

When the signal charge Q3 is added to the signal charge Q2, the reset transistor 34 resets the detection node 33 as is illustrated in FIG. 4F. The detection node 33 is therefore set at the initial potential.

FIGS. 5 and 6 are timing charts illustrating the operation of the image pickup apparatus, which has been described with reference to FIGS. 4A to 4F.

FIG. 5 shows how the cells of different rows operate during the same horizontal blanking period. In the same horizontal blanking period, signals are added (FIG. 4E) in each unit cell of one row and a signal is read from the unit cell to the vertical line (FIG. 4F), whereas in each unit cell of another row, a part of a signal charge is skimmed from the detection node (FIGS. 4B and 4C). In each unit cell of any row, a

part of the signal charge is skimmed from the detection node during a horizontal blanking period, and signals are added and a signal is read to the vertical signal line during another horizontal blanking period.

5 FIG. 6 is a timing chart illustrating how the image pickup apparatus of FIG. 1 operates, mainly during a vertical period. As can be understood from FIG. 6, signals are read from the image sensor in units of frames. Signal charges are accumulated in first and
10 second accumulation periods. The accumulation of signal charges proceeds in the vertical direction, or along the Y axis of FIG. 6, which corresponds to the vertical direction of the image sensor 11 illustrated in FIG. 1. Of the signal charges accumulated in the
15 first accumulation period, those larger than a preset value are skimmed from the detection nodes. The signal charge accumulated in each unit cell during the second accumulation period is added to the signal charge accumulated the unit cell during the first accumulation
20 period. The signals obtained by this addition in the unit cells of each row are sequentially read, at the times indicated by slant solid lines in FIG. 6. The simultaneity of reading image signals is important, particularly if the image is one that moves fast. The
25 simultaneity is not important when the frame frequency is 30 Hz or more.

As described above, those of the signal charges

accumulated in the first accumulation period, which are larger than a preset value, are skimmed in the present embodiment. In addition, the first accumulation period is longer than the second accumulation period. Therefore, the image sensor 11 can exhibit two different photoelectric conversion characteristics, one for light beams of standard intensity and the other for light beams of high intensity. This helps increase the dynamic range of the solid-state image pickup apparatus.

In the above embodiment, the accumulation period is divided into two sub-periods, it may be possible that the accumulation period is divided into three or more sub-periods.

(Second Embodiment)

The second embodiment of the invention, which is a solid-state image pickup apparatus of amplification type, will be described with reference to the timing charts of FIGS. 7 and 8. The second embodiment is designed for use in electronic still cameras having a mechanical shutter each.

FIG. 7 explains how this solid-state image pickup apparatus operates if the photodiode provided in each unit cell is of incomplete transfer type, such as an np-type photodiode.

Assume that the image sensor pick up an ordinary image and that the unit cells of the image sensor generates signals. In this case, the signals are read

from the image sensor in units of frames. After
reading the signals from the image sensor, the image
sensor is reset, as is required because the photodiodes
are of incomplete transfer type, such as np-type
5 photodiodes. The signal charges are thereby extracted
from the photodiodes, thus skimming bias charges
therefrom.

After resetting the image sensor, the mechanical
shutter is opened. The shutter remains open for a
10 period of time, during which light illuminated signals
are stored in the image sensor. The shutter is
thereafter closed to read signals from the image sensor.
The image sensor is reset again, then dark signals
generated while the shutter remains closed are stored
15 in the image sensor. Then, the dark signals are read
out from the image sensor. The difference between each
dark signal and the corresponding light illuminated
signal is obtained, thereby removing noises such as
background irregularities and white defects. If these
20 noises are not so prominent, it is unnecessary to read
the dark signals.

FIG. 8 explains how the solid-state image pickup
apparatus, which is the second embodiment, operates
if the photodiode provided in each unit cell is of
25 complete transfer type.

Since the photodiodes are of complete transfer
type, image lags or linearity problem will not arise.

Hence, it is unnecessary to reset the image sensor as is illustrated in FIG. 7. Therefore, as shown in FIG. 8, signals are read from the unit cells also during the period that corresponds to the reset period (FIG. 7). More precisely, light illuminated signals are accumulated in the image sensor, then the light illuminated signals are read from the image sensor. After the light illuminated signals have been read, dark signals are accumulated in the image sensor, then the dark signals are read from the image sensor. The difference between each light illuminated signal and the corresponding dark signal is obtained, thereby removing noises such as background irregularities and white defects. If these noises are not so prominent, it is unnecessary to store the dark signals into the line memory, as in the case where the photodiodes are of incomplete transfer type.

(Third Embodiment)

The third embodiment of this invention, which is also a solid-state image pickup apparatus of amplification type, will be described with reference to the timing charts of FIG. 9. The unit cells of the image sensor incorporated in the third embodiment have a structure similar to the one depicted in FIG. 2.

As shown in FIG. 9, signals are read from the unit cells of the Xth row and (X+Y)th row during the same horizontal blanking period. During the next horizontal

blanking period, signals are read from the unit cells of the $(X+1)$ th and $(X+1+Y)$ th row.

Assume that T rows of unit cells serve to accumulate signal charges for one frame. In this case, the signal accumulated in any unit cell for the period corresponding to $(T-Y)$ rows is read into the line memory during a horizontal blanking period, and the signal accumulated in the same unit cell for the period corresponding to Y rows is read into the line memory during another horizontal blanking period. These two signals are read from the line memory at the same time and added by a signal-processing circuit provided outside the image pickup apparatus (i.e., one-chip apparatus). Since the periods differ in length, the dynamic range of the image pickup apparatus can be increased.

FIG. 10 shows the case where signals are read from the unit cells of the X th row, $(X+Y)$ th row and $(X+Y+Z)$ th row during the same horizontal blanking period.

Assume that T rows of unit cells serve to accumulate signal charges for one frame. Then, the signal accumulated in any unit cell for the period corresponding to $(T-Y-Z)$ rows is read into the line memory during a horizontal blanking period. The signal accumulated in the same unit cell for the period corresponding to Y rows is read into the line memory

during another horizontal blanking period. The signal accumulated in the same unit cell for the period corresponding to Z rows is read into the line memory during still another horizontal blanking period. These
5 signals are read from the line memory at the same time and added by a signal-processing circuit provided outside the image pickup apparatus (i.e., one-chip apparatus). In this case, too, the periods differ in length, and the dynamic range of the image pickup
10 apparatus can be increased. This method of reading signals can be applied because random access is possible with a solid-state image pickup apparatus of amplification type.

FIG. 11 explains how the third embodiment operates,
15 mainly during the vertical period. Signals are read from the unit cells during the period between a vertical blanking period and a next vertical blanking period. The accumulation period is divided into two sub-periods x and y . The signal accumulated in a unit
20 cell during the relatively long period x is read to the vertical signal line during a horizontal blanking period. The signal accumulated in the unit cell during the relatively short period y is read to the vertical
25 signal line during a different horizontal blanking period. These signals, thus read, from the unit cell, are stored into line memories, respectively. The Y-axis of the graph showing the accumulation periods x

and y corresponds to the vertical direction of the image sensor 11 (FIG. 1), along which the accumulation of signal charges proceeds.

FIG. 12 explains how the third embodiment operates, mainly during the vertical period, in another manner. The accumulation period is divided into three sub-periods x, y and z. The signal accumulated in a unit cell during the relatively long period x is read to the vertical signal line during a horizontal blanking period. The signal accumulated in the unit cell during the relatively short period y is read to the vertical signal line during another horizontal blanking period. The signal accumulated in the unit cell during the period z shorter than the period y is read to the vertical signal line during still another horizontal blanking period. These signals, thus read, from the unit cell, are stored into line memories, respectively.

FIG. 13 is a schematic representation of the third embodiment of the invention. The third embodiment is similar to the first embodiment (FIG. 1) in basic structure. The solid-state image pickup apparatus shown in FIG. 13 is so designed that signals are read from the unit cells in such a manner as has been explained with reference to FIGS. 10 and 12.

In this apparatus, a one-frame period is divided into three sub-periods x, y and z. The signals are read from different rows of unit cells to the vertical

signal lines during the same horizontal blanking period when the vertical register 13 supplies a control signal to the image sensor 11. The signals read to the vertical lines are temporarily stored in the line memories 12a, 12b and 12c. (The line memories are provided, each for storing a one-line data.) The signals are read from the line memories 12a, 12b and 12c when the horizontal register 14 supplies a signal to the line memories 12a, 12b and 12c. The signals thus read are stored respectively into frame memories 17a, 17b and 17c, respectively. The frame memories 17a to 17c are provided outside the chip 10 in which the image sensor 11, line memories 12a to 12c, vertical register 13, horizontal register 14 and timing generator 15 are provided. Of the signals stored in the frame memories 17a to 17c, those read from the same unit cell during different horizontal blanking periods (i.e., signals accumulated during different periods) are added by an adder circuit 18, which generates a sum signal. An image will be synthesized from the sum signal.

The photoelectric conversion characteristic of the third embodiment will be explained with reference to FIGS. 14 and 15.

As shown in FIG. 14, the image sensor 11 exhibits photoelectric conversion characteristic C1 during the accumulation sub-period x, photoelectric conversion

characteristic C2 during the accumulation sub-period y,
and photoelectric conversion characteristic C3 during
the accumulation sub-period z. As can be seen from
FIG. 14, the image sensor 11 generates the largest
5 output during the accumulation sub-period x. The
sensor 11 generates the second largest output during
the accumulation sub-period y and the smallest output
during accumulation sub-period z.

Generally, a photodiode cannot accumulate more
10 charge once it has accumulated a saturated signal
charge. Hence, any image pickup apparatus that has
unit cells each having a photodiode has such a dynamic
range DR as is illustrated in FIG. 14. The dynamic
range DR is about 60 to 70 dB at most.

15 By contrast the synthesized signal generated by
the signal-processing circuit shown in FIG. 13, which
comprises the frame memories 17a to 17c and the adder
circuit 18, has a longer dynamic range DR as is
illustrated in FIG. 15. As may be understood from
20 FIG. 15, that part of the synthesized signal which
ranges up to output level L1 corresponds to the
photoelectric conversion characteristic C1. That part
of the synthesized signal, which ranges between output
levels L1 and L2 corresponds to the photoelectric
25 conversion characteristic C2. That part of the
synthesized signal, which is above output level L2,
corresponds to the photoelectric conversion

characteristic C3.

Thus, in the third embodiment has the dynamic range DR sown in FIG. 14 is increased to the dynamic range DR shown in FIG. 15.

5 (Fourth Embodiment)

FIG. 16 is a sectional view showing mainly one of the unit cells incorporated in an image pickup apparatus that is the fourth embodiment of the invention. The unit cell is similar to the one shown in FIG. 3 in terms of basic structure.

As shown in FIG. 16, the unit cell comprises a photodiode 41, a transfer (read) transistor 42, and a detection node 43. (The photodiode 41 has a p-type region in the surface of a charge-accumulating region of n type.) The photodiode 41, transistor 42 and node 43 are provided on an N-type silicon substrate 40 that has a P-type well in the surface. The unit cell is electrically isolated from any other unit cell by a LOCOS element-isolating region 44.

20 A signal charge is transferred from the photodiode 41 to the detection node 43 via the transfer transistor 42. The detection node 43 has parasitic capacitance, which converts the signal charge to a signal voltage. The signal voltage is applied via an amplifier 45 to a
25 signal line 46 and is stored in a line memory (not shown).

FIG. 17 is a timing chart explaining the operation

of the fourth embodiment having the unit cells
(FIG. 16).

In the first to third embodiments described above,
data is read from the unit cells, line by line. Thus,
5 it is easy to scan the unit cells. However, a one-
frame time lag inevitably occurs between the first line
and the last line. The simultaneity, in the first to
third embodiments, is therefore lower than is possible
with CCD-type solid-state image pickup apparatuses.

10 This give rise to no problems if the frame frequency is
sufficiently high or the image picked up and reproduced
is one that moves slowly. If the frame frequency is
low or the image is one that moves fast, the image
reproduced will be blurred.

15 The fourth embodiment is a solid-state image
pickup apparatus of amplification type in which can
pick up images with high simultaneity (start time and
end time for accumulating charges are same in all unit
cells). More specifically, the unit cells of all rows
20 (lines) are operated during same vertical blanking
period as illustrated in FIG. 17. As FIG. 17 shows,
the unit cells are operated three times during each
vertical blanking period. Instead, they may be
operated once, twice, four times, or more times during
25 each vertical blanking period.

It will be described how the unit cells are
operated only once during each vertical blanking period.

First, all detection nodes 43 are reset. Further, all transfer transistors 42 are turned on. A signal charge is thereby supplied to each detection node 43 and accumulated in the parasitic capacitance of the node 43.

5 To increase capacitance to hold the signal charge, a capacitor may be connected in parallel to the parasitic capacitance. Thus, all signal charges are accumulated in the respective detection nodes 43 during same vertical blanking period. The signal charges are sequentially read, line by line, from the image sensor during the horizontal blanking period included in the vertical effect period that follows the vertical blanking period. As a result, the fourth embodiment can pick up an image with as high simultaneity as is possible with CCD-type solid-state image pickup apparatuses.

10

15

With reference to the timing chart of FIG. 17, it will be described how the unit cells are operated three times during same vertical blanking period. In this case, too, the unit cells of all rows (lines) are operated during same vertical blanking period in order to pick up an image with high simultaneity. Further, a part of the signal charge is skimmed from each detection node and signals are added in the detection node 43, as in the first embodiment, thereby increasing the dynamic range of the image pickup apparatus. That is, the first accumulation is effected during period T1,

20

25

the second accumulation during period T2, and the third accumulation during period T3, as is illustrated in FIG. 17.

The operation of the fourth embodiment will be explained in greater detail, with reference to FIGS. 18 and 19. FIG. 18 is a timing chart showing how signal charges are accumulated twice during a one-frame period. FIG. 19 is a timing chart explaining how signal charges are accumulated three times during a one-frame period.

How signal charges are accumulated twice during a one-frame period will be described with reference to FIG. 18. First, that part of the signal charge accumulated in the detection node 43 during the accumulation period T1, which is at a voltage higher than a voltage V1, is skimmed from the detection node 43 as in the first embodiment in the vertical blanking period. No parts of the signal charge will be skimmed if the voltage built up by the signal charge is lower than the voltage V1. If the voltage is higher than the voltage V1, the output signal of the detection node 43 will be set to the voltage V1 at a time T1. Then, the signal charge accumulated in the photodiode 41 in the period T2 is added to the signal charge held in the detection node 43, whereby a voltage signal is generated. This voltage signal is read from the unit cell during the vertical blanking period. Thus, a part of a signal charge is skimmed and signal charges

are added in the unit cells of all rows (all lines) during the same vertical blanking period. In this respect, the fourth embodiment differs from the first embodiment in which a part of a signal charge is skimmed and signal charges are added in the unit cells of one row, then in the unit cells of the next row, and so on.

How signal charges are accumulated three times during a one-frame period will be described with reference to FIG. 19. First, that part of the signal charge accumulated in the detection node 43 during accumulation period T1, which is at a voltage higher than a voltage V1, is skimmed from the detection node 43 as in the first embodiment in the vertical blanking period. Thereafter, the signal charge accumulated in the photodiode 41 in accumulation period T2 is added to the signal charge that corresponds to the voltage V1. Of the signal charge held in the detection node 42, the part which corresponds to a voltage higher than voltage V3 is skimmed during the vertical blanking period. Then, the signal charge accumulated in the photodiode during accumulation period T3 is added to the signal charge held in the detection node 43. A voltage signal is thereby generated. This voltage signal is read from the unit cell during the same vertical blanking period.

The solid-state image pickup apparatus of amplification type according to the fourth embodiment

can not only have an increased dynamic range, but also satisfy a high simultaneity.

Additional advantages and modifications will readily occur to those skilled in the art. Therefore,
5 the invention in its broader aspects is not limited to the specific details and representative embodiments shown and described herein. Accordingly, various modifications may be made without departing from the spirit or scope of the general inventive concept as
10 defined by the appended claims and their equivalents.

CLAIMS

1. An image pickup apparatus comprising:
an array of unit cells arranged in rows and
columns, each unit cell having a light-receiving device
5 for receiving light and generating an electric charge
corresponding to the light, a charge-accumulating
section for accumulating the electric charge generated
by the light-receiving device, a transfer device for
transferring the electric charge from the light-
10 receiving device to the charge-accumulating section,
and a charge-limiting device for limiting the electric
charge accumulated in the charge-accumulating section;
a plurality of vertical signal lines extending
along the columns of unit cells, respectively, each for
15 receiving a electric data item corresponding to the
electric charge accumulated in the charge-accumulating
section of any unit cell of the associated column; and
a control circuit for controlling each of the unit
cells, causing the charge-limiting device to limit the
20 charge generated by the light-receiving device during a
first period and transferred to the charge-accumulating
section through the transfer device and, adding, to the
electric charge accumulated in the charge-accumulating
section, the charge generated by the light-receiving
25 device during a second period following the first
period and transferred to the charge-accumulating
section through the transfer device.

2. An image pickup apparatus according to claim 1,
wherein the control circuit controls each of the unit
cells of the same row such that the charge-limiting
device limits the charge accumulated in the charge-
5 accumulating section, during a horizontal blanking
period, and the charge transferred to the charge-
accumulating section is added to the charge accumulated
in the charge-accumulating section, during a different
horizontal blanking period.

10 3. An image pickup apparatus according to claim 1,
wherein the control circuit controls the unit cells
such that the charge-limiting device limits the charge
to be accumulated in the charge-accumulating section
and the charge transferred to the charge-accumulating
15 section is added to the charge accumulated in the
charge-accumulating section, during the same vertical
blanking period in all cell units.

4. An image pickup apparatus according to claim 1,
wherein the first period is longer than the second
20 period.

5. An image pickup apparatus comprising:
an array of unit cells arranged in rows and
columns, each unit cell having a light-receiving device
for receiving light and generating an electric charge
25 corresponding to the light, a charge-accumulating
section for accumulating the electric charge generated
by the light-receiving device, a first transistor

having a first control terminal and connected between
the light-receiving device and the charge-accumulating
section, for transferring the charge generated in the
light-receiving device to the charge-accumulating
5 section when a transfer signal is supplied to the first
control terminal, and a second transistor having a
second control terminal and connected to the charge-
accumulating section, for limiting the charge
accumulated in the charge-accumulating section when a
10 voltage between off voltage and on voltage is applied
to the second terminal;

a plurality of vertical signal lines extending
along the columns of unit cells, respectively, each for
receiving a electric data item corresponding to the
electric charge accumulated in the charge-accumulating
15 section of any unit cell of the associated column; and

a control circuit for controlling each of the unit
cells, causing the second transistor to limit the
charge generated by the light-receiving device during a
20 first period and transferred to the charge-accumulating
section through the first transistor, and adding, to
the electric charge accumulated in the charge-
accumulating section, the charge generated by the
light-receiving device during a second period following
25 the first period and transferred to the charge-
accumulating section through the first transistor.

6. A method of controlling an image pickup

apparatus comprising: an array of unit cells arranged in rows and columns, each unit cell having a light-receiving device for receiving light and generating an electric charge corresponding to the light, a charge-accumulating section for accumulating the electric charge generated by the light-receiving device, a transfer device for transferring the electric charge from the light-receiving device to the charge-accumulating section, and a charge-limiting device for limiting the electric charge accumulated in the charge-accumulating section; a plurality of vertical signal lines extending along the columns of unit cells, respectively, each for receiving a electric data item corresponding to the electric charge accumulated in the charge-accumulating section of any unit cell of the associated column, said method comprising the steps of:

transferring the charge generated by the light-receiving device during a first period to the charge-accumulating section through the transfer device;

limiting the charge transferred to the charge-accumulating section, by means of the charge-limiting device; and

transferring the charge generated by the light-receiving device during a second period following the first period, to the charge-accumulating section through the transfer device, thereby adding the charge generated during the second period to the charge

limited by the charge-limiting device.

7. An image pickup apparatus comprising:

an array of unit cells arranged in rows and columns, each having a light-receiving device for receiving light and generating an electric charge corresponding to the light;

a plurality of vertical signal lines extending along the columns of unit cells, respectively, each for receiving a electric data item corresponding to the electric charge generated by the light-receiving device of any unit cell of the associated column;

a control circuit for controlling each of the unit cells of the same row such that electric data items corresponding to electric charges generated by the light-receiving device during different periods, respectively, are read through the vertical signal line; and

a data-holding circuit for temporarily holding the electric data items read by the control circuit, so as to add the electric data items in an external circuit.

8. An image pickup apparatus according to claim 7, wherein the control circuit controls each of the unit cells of the same row such that the electric data items are read through the vertical signal line during different horizontal blanking periods, respectively.

9. An image pickup apparatus according to claim 7, wherein the different periods differ in length.

10. An image pickup apparatus comprising:

an array of unit cells arranged in rows and columns, each having a light-receiving device for receiving light and generating an electric charge corresponding to the light;

a plurality of vertical signal lines extending along the columns of unit cells, respectively, each for receiving a electric data item corresponding to the electric charge generated by the light-receiving device of any unit cell of the associated column;

a vertical control circuit for controlling the unit cells such that electric data items generated by the unit cells of different rows are read to the vertical signal lines during the same horizontal blanking period, and that electric data items corresponding to electric charges generated by the light-receiving device of each unit cell of the same row during different periods, respectively, are read through the vertical signal lines during different horizontal blanking periods;

a plurality of data-holding circuits for temporarily holding the electric data items read from the unit cells of different rows during the same horizontal blanking period under the control of the vertical control circuit;

a horizontal control circuit for controlling reading of the electric data items from the

data-holding circuits;

a storage circuit for storing the electric data items read from the data-holding circuits under the control of the horizontal control circuit; and

- 5 an adder circuit for adding those electric data items stored in the storage circuit, which correspond to the charges generated by the same light-receiving device during different periods.

ABSTRACT OF THE DISCLOSURE

An image pickup apparatus comprising an array of unit cells, vertical signal lines, and a control circuit. The unit cells are arranged in rows and columns. Each unit cell has a light-receiving device for receiving light and generating an electric charge corresponding to the light, a charge-accumulating section for accumulating the electric charge generated by the light-receiving device, a transfer device for transferring the electric charge from the light-receiving device to the charge-accumulating section, and a charge-limiting device for limiting the electric charge accumulated in the charge-accumulating section. The vertical signal lines extend along the columns of unit cells, respectively, each for receiving a electric data item corresponding to the electric charge accumulated in the charge-accumulating section of any unit cell of the associated column. The control circuit controls each of the unit cells, causing the charge-limiting device to limit the charge generated by the light-receiving device during a first period and transferred to the charge-accumulating section through the transfer device. The charge generated by the light-receiving device during a second period following the first period and transferred to the charge-accumulating section through the transfer device is added to the electric charge accumulated in the charge-accumulating section.

FIG. 1

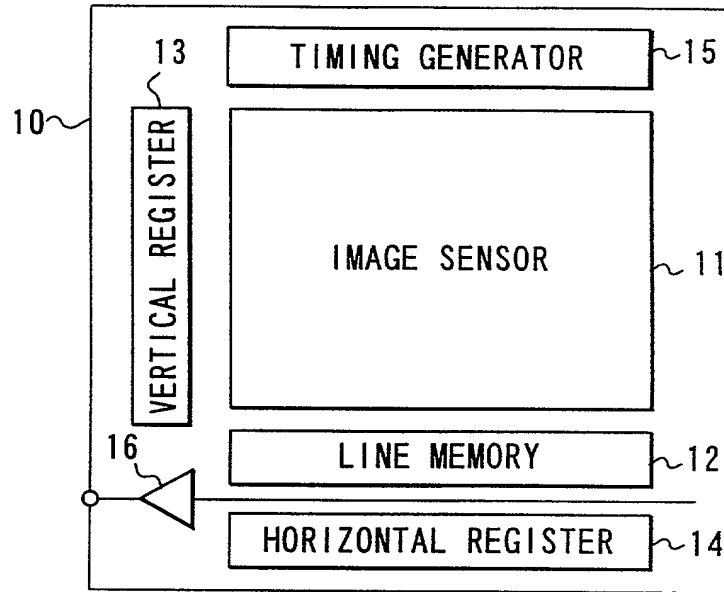


FIG. 2

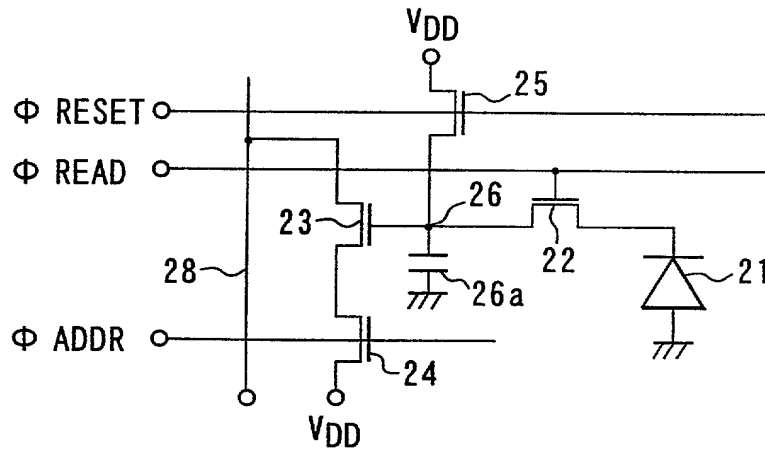


FIG. 3

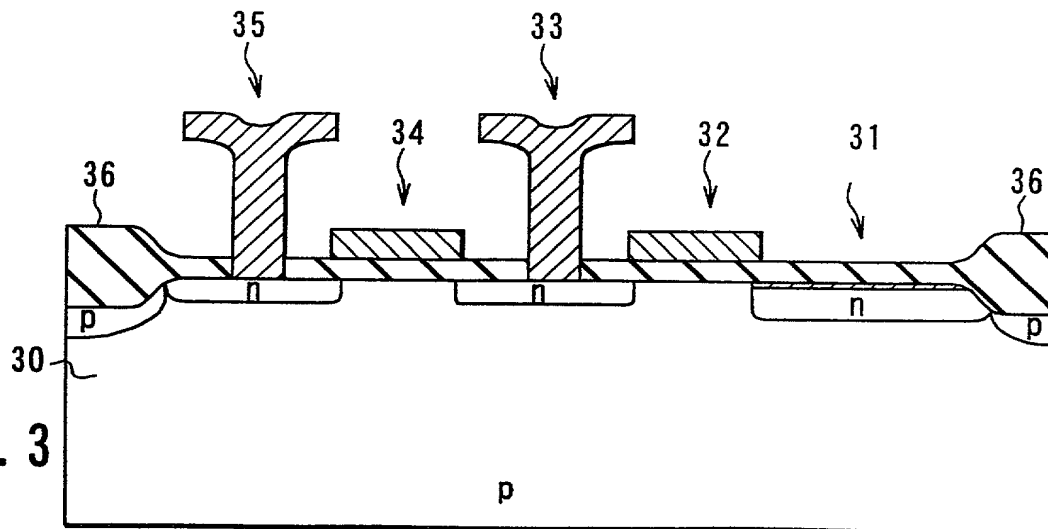


FIG. 4A

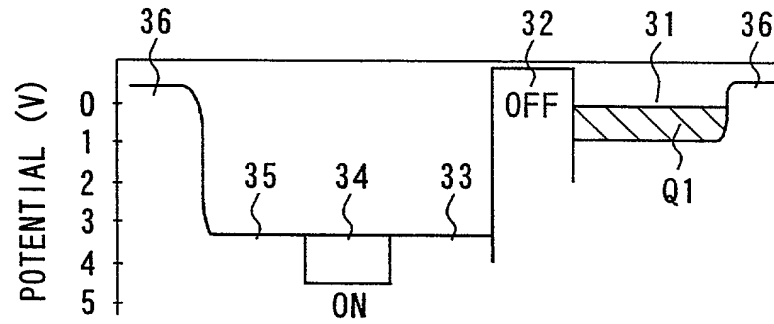


FIG. 4B

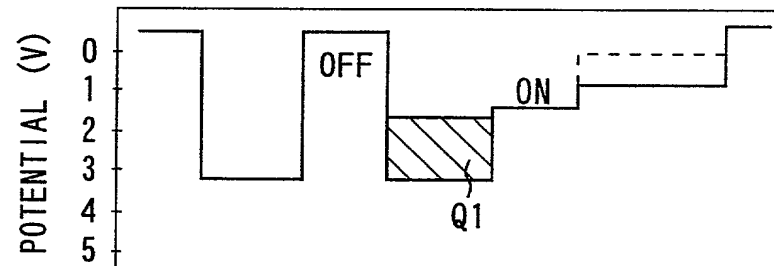


FIG. 4C

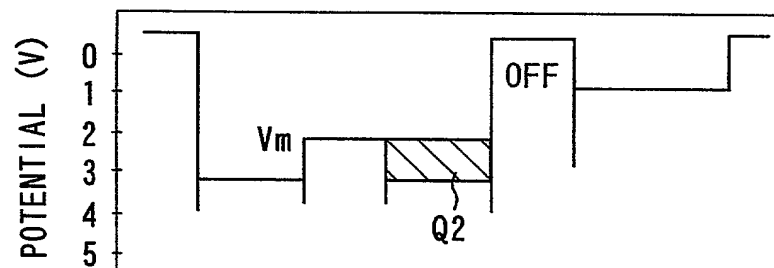


FIG. 4D

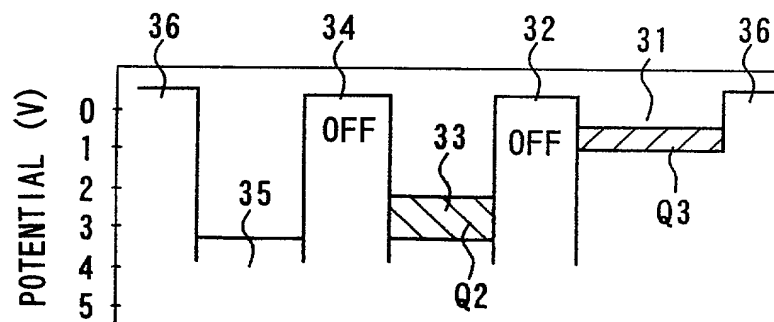


FIG. 4E

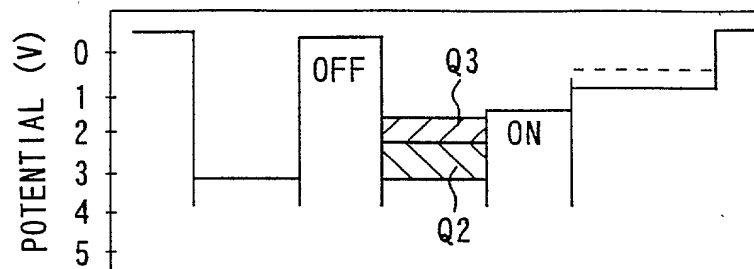


FIG. 4F

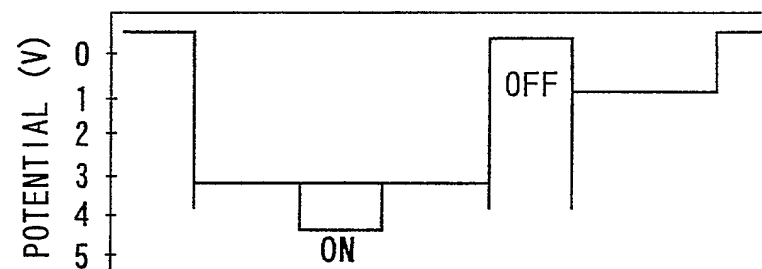


FIG. 5

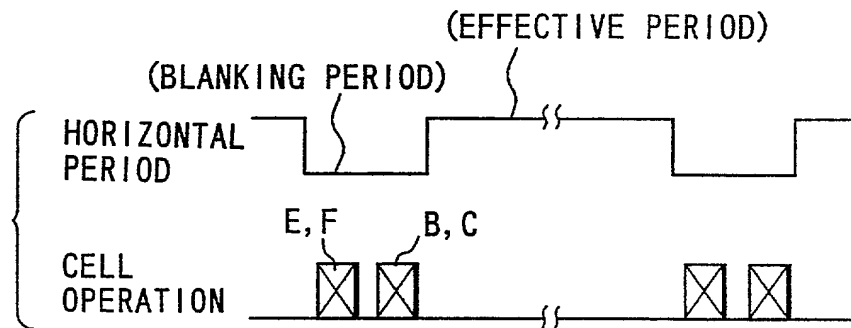
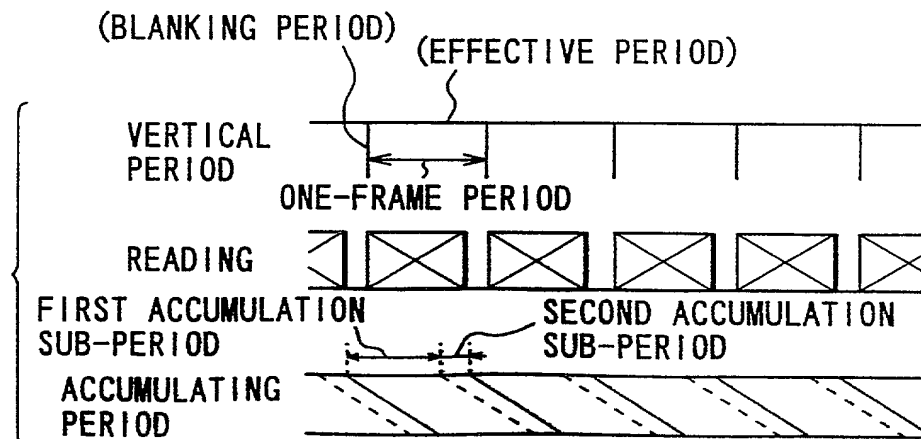


FIG. 6



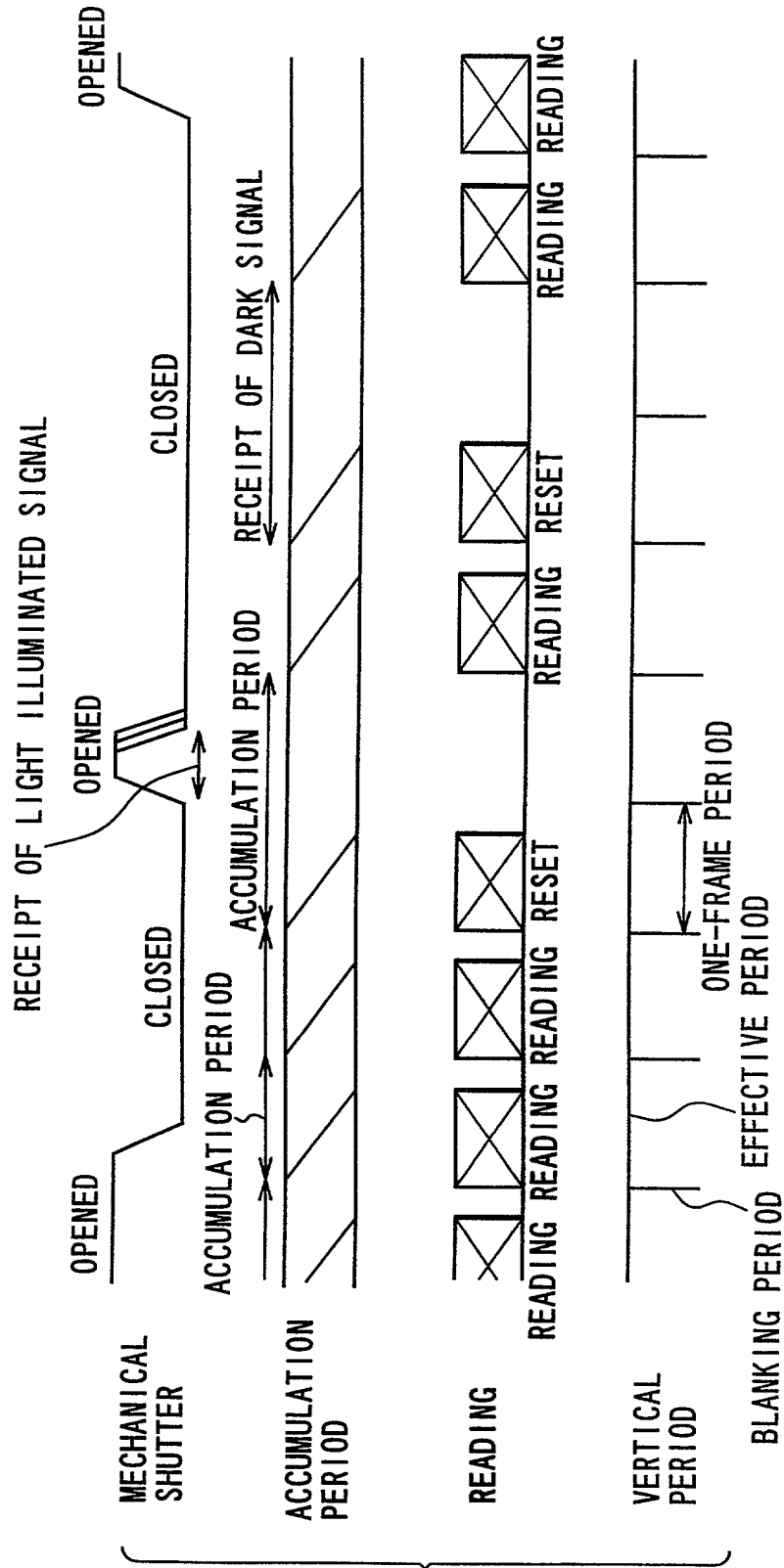


FIG. 7

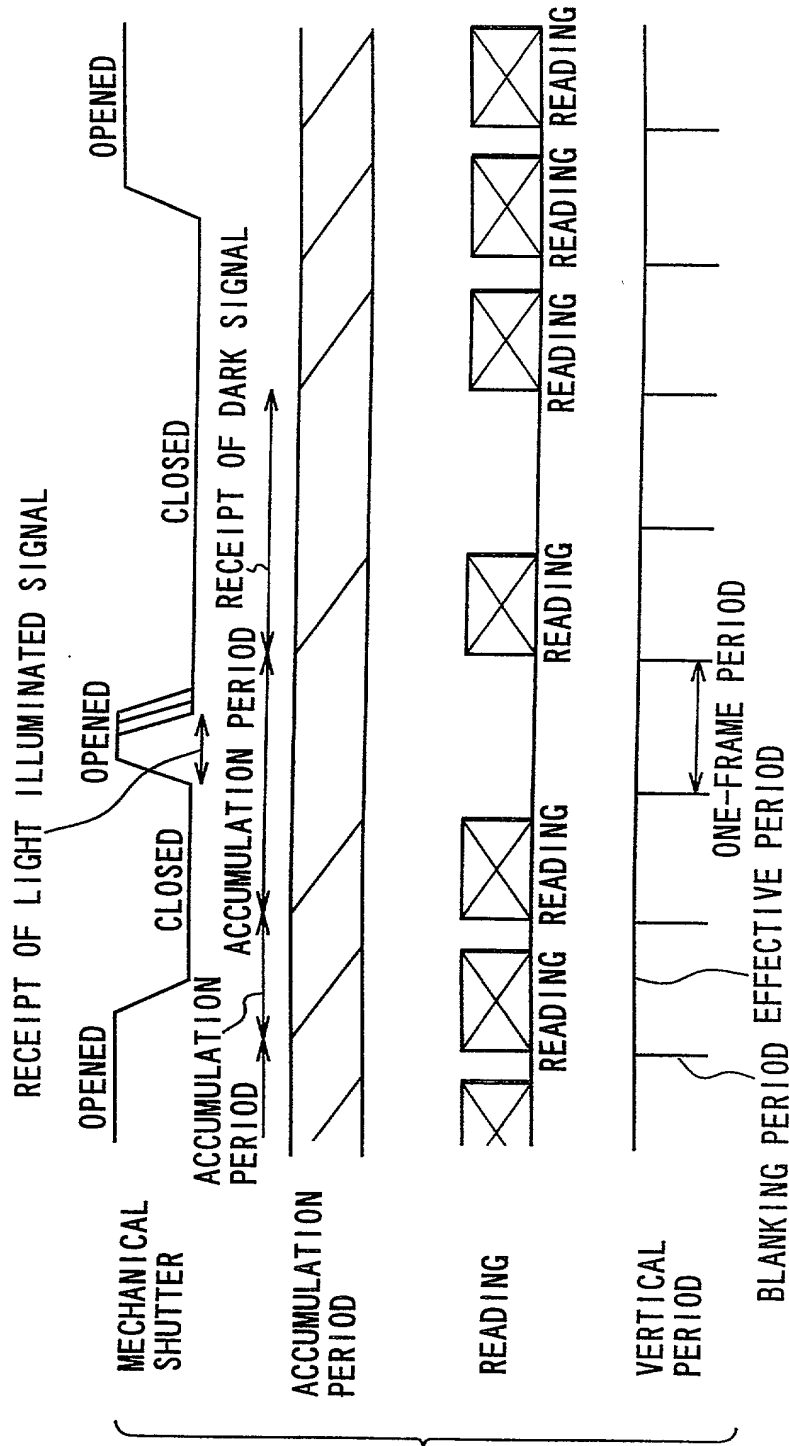
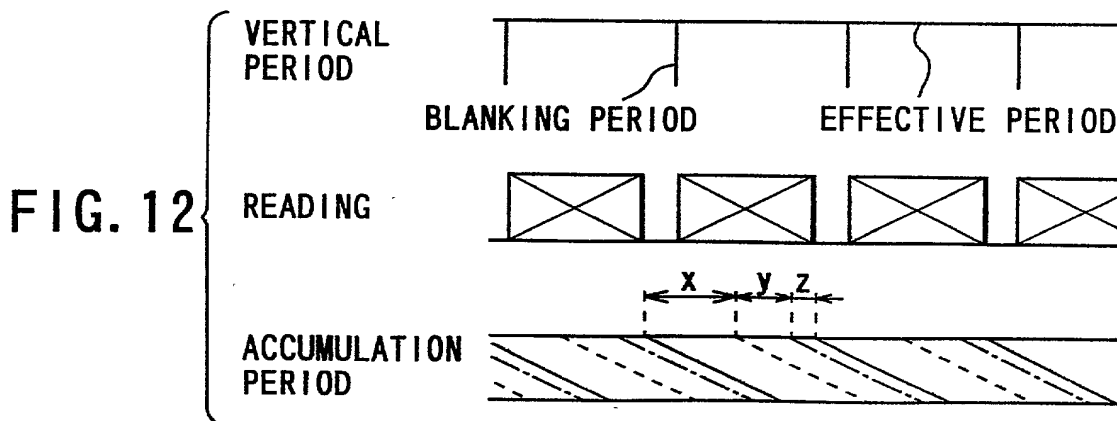
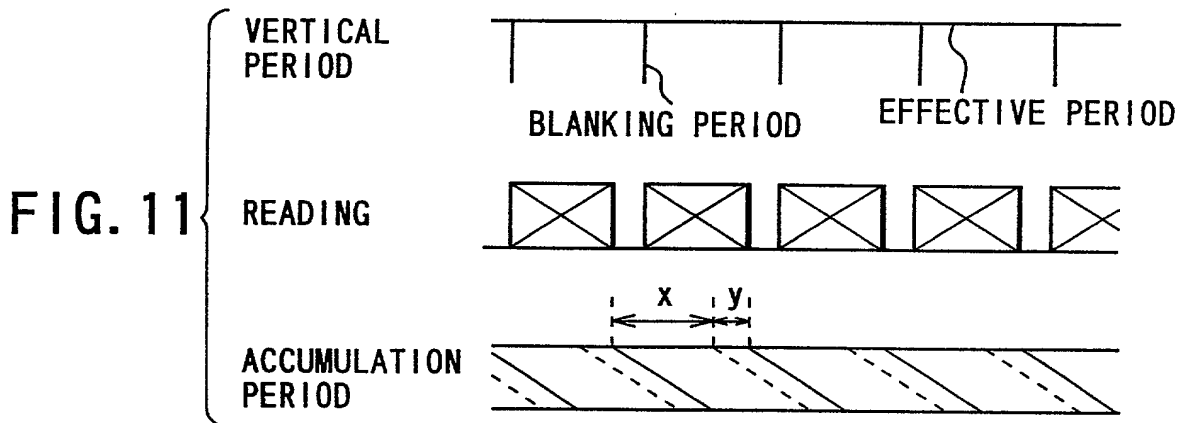
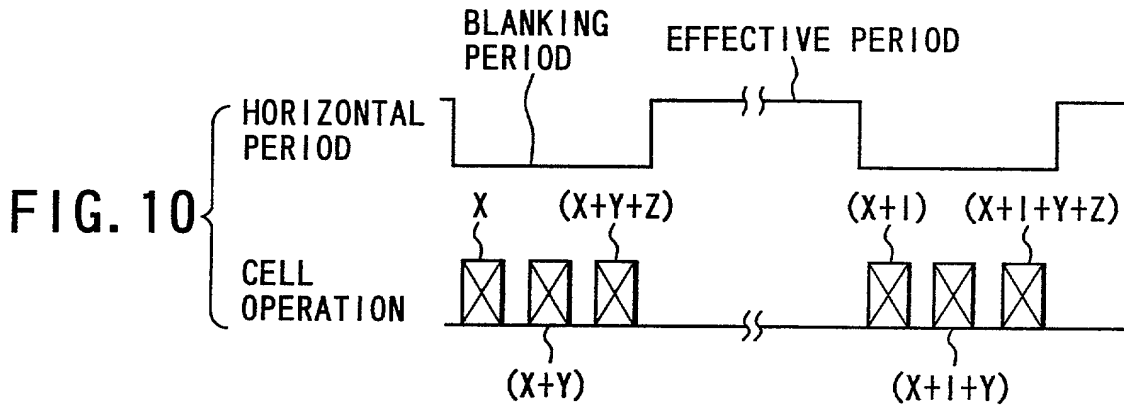
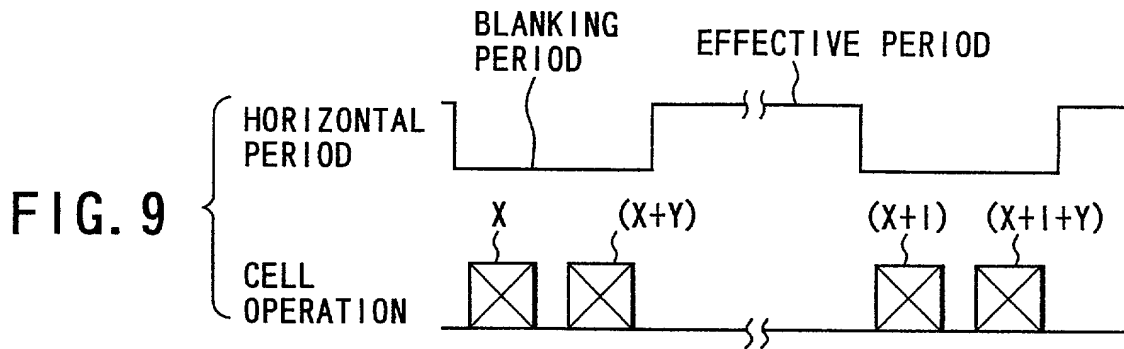


FIG. 8



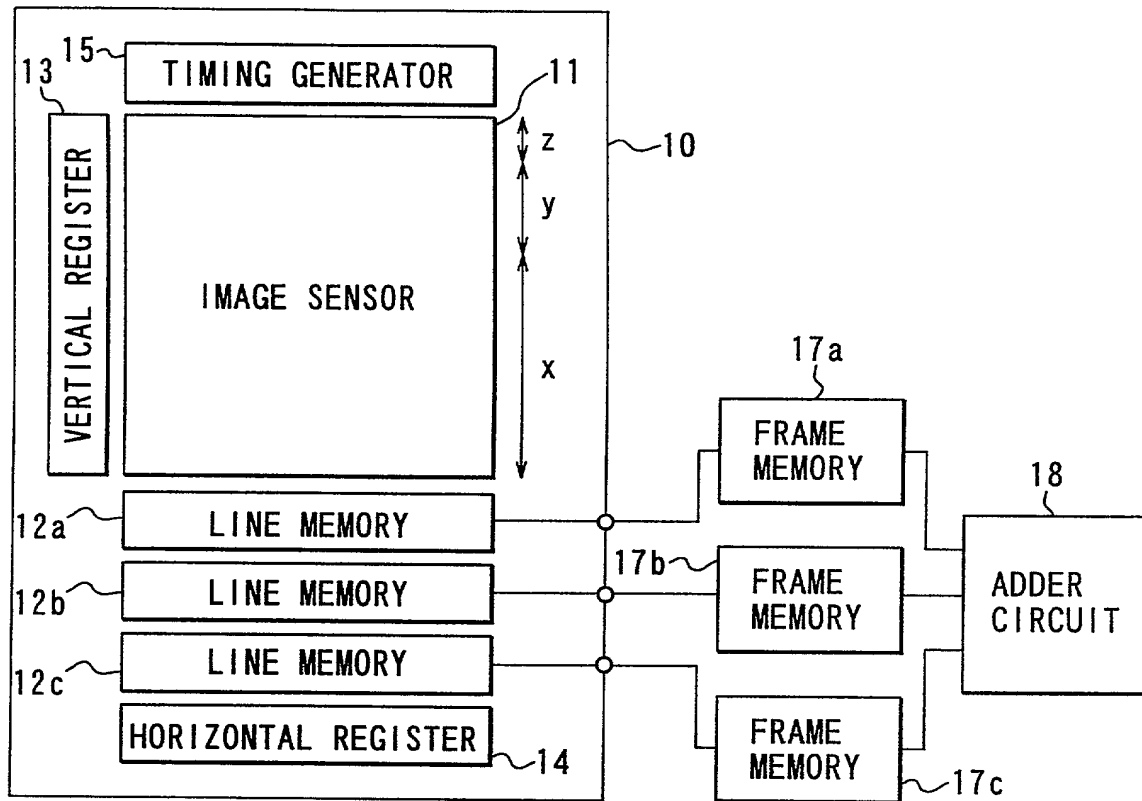
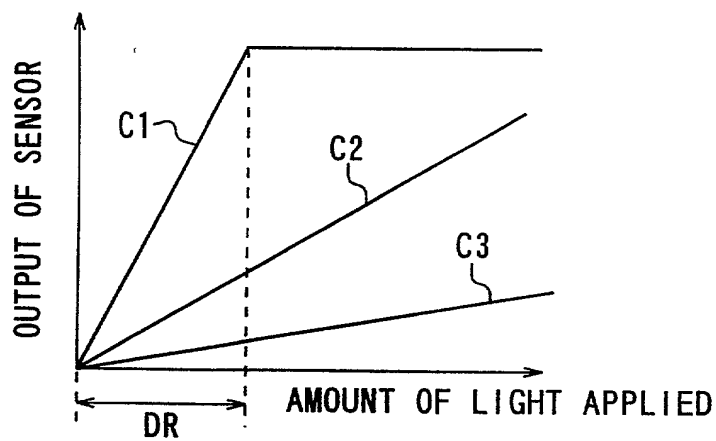
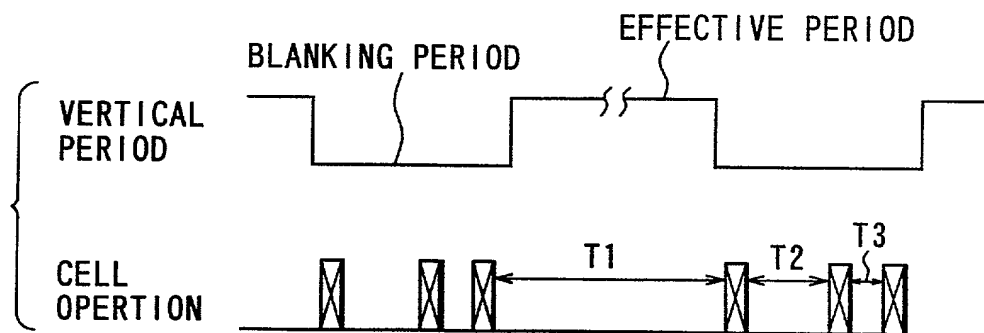
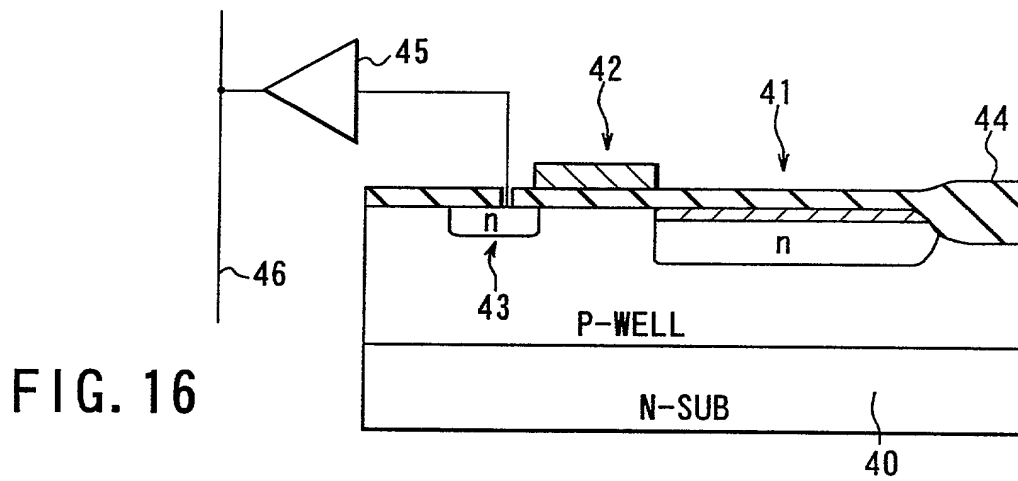
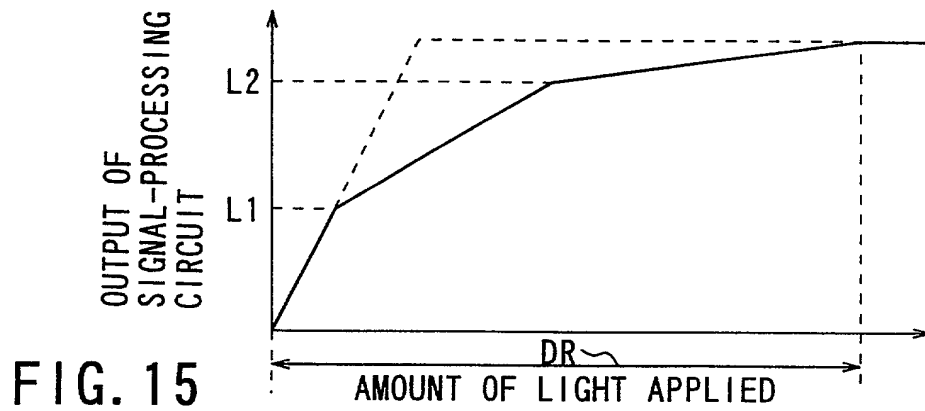


FIG. 13

FIG. 14





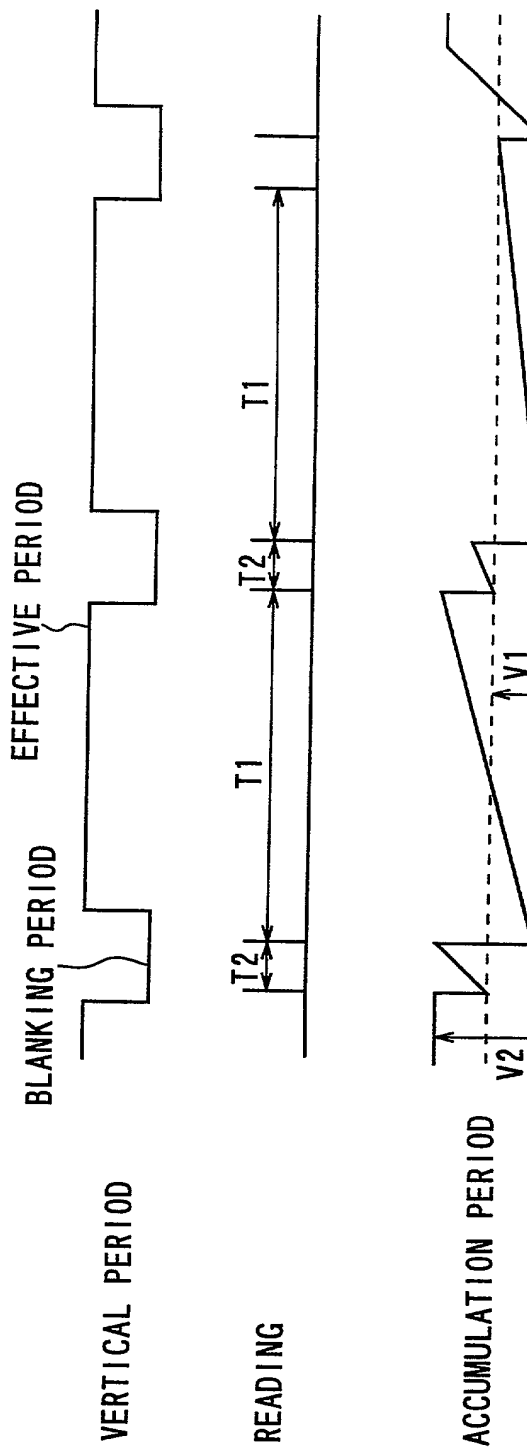


FIG. 18

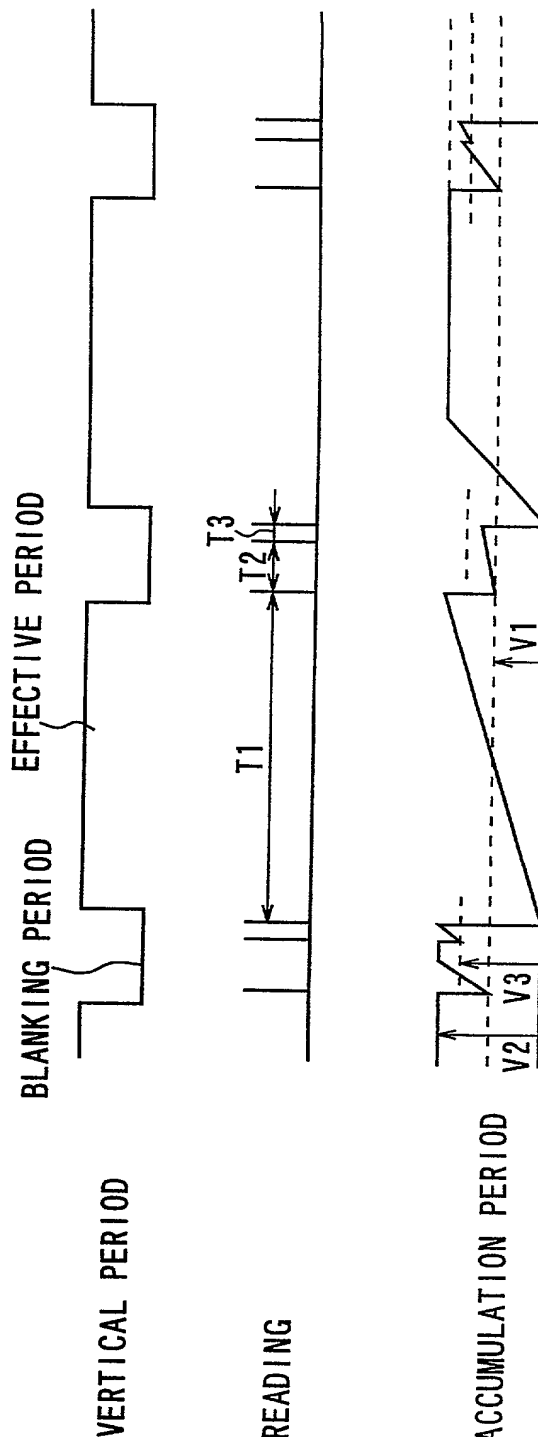


FIG. 19

DECLARATION FOR PATENT APPLICATION

As a below named inventor, I declare:

that I verily believe myself to be the original, first and sole (if only one individual inventor is listed below) or an original, first and joint inventor (if more than one individual inventor is listed below) of the invention in

**IMAGE PICKUP APPARATUS AND METHOD OF CONTROLLING THE
IMAGE PICKUP APPARATUS**

the specification of which is attached hereto unless the following box is checked.

☐ was filed on _____ as United States Application
or PCT International Application No. _____, and
was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information of which is material to patentability as defined in 37 CFR 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or 365 (b) of any foreign application(s) for patent or inventor's certificate, or 35 U.S.C. 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed:

<u>Country</u>	<u>Category</u>	<u>Application No.</u>	<u>Filing Date</u>	<u>Priority Claim</u>
Japan	Patent	10-185121	June 30, 1998	Yes

And I hereby appoint Norman F. Oblon (Reg. No. 24,618), Marvin J. Spivak (Reg. No. 24,913), C. Irvin McClelland (Reg. No. 21,124), Gregory J. Maier (Reg. No. 25,599), Arthur I. Neustadt (Reg. No. 24,854), Richard D. Kelly (Reg. No. 27,757), James D. Hamilton (Reg. No. 28,421), Eckhard H. Kuesters (Reg. No. 28,870), Robert T. Pous (Reg. No. 29,099), Charles L. Gholz (Reg. No. 26,395), Vincent J. Sunderdick (Reg. No. 29,004), William E. Beaumont (Reg. No. 30,996), Steven B. Kelber (Reg. No. 30,073), Robert F. Gnuse (Reg. No. 27,295), Jean-Paul Lavalleye (Reg. No. 31,451), Stephen G. Baxter (Reg. No. 32,884), Martin M. Zoltick (Reg. No. 35,745), Robert W. Hahl (Reg. No. 33,893), Richard L. Treanor (Reg. No. 36,379), Steven P. Weihrouch (Reg. No. 32,829), John T. Goolkasian (Reg. No. 26,142), Marc R. Labgold (Reg. No. 34,651), Richard L. Chinn (Reg. No. 34,305), Steven E. Lipman (Reg. No. 30,011), Carl E. Schlier (Reg. No. 34,426), James J. Kulbaski (Reg. No. 34,648), Catherine B. Richardson (Reg. No. 39,007), Richard A. Neifeld (Reg. No. 35,299), J. Derek Mason (Reg. No. 35,270) and Surinder Sachar (Reg. No. 34,423), each of whose address is Fourth Floor, 1755 Jefferson Davis Highway, Arlington, Virginia 22202, or any one of them, my attorneys with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent & Trademark Office connected therewith, and request that correspondence be directed to Oblon, Spivak, McClelland, Maier & Neustadt, P.C., Fourth Floor, 1755 Jefferson Davis Highway, Arlington, Virginia 22202.

I declare further that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

DECLARATION FOR PATENT APPLICATION

I declare further that my post office address is at c/o Intellectual Property Division, KABUSHIKI KAISHA TOSHIBA, 1-1 Shibaura 1-chome, Minato-ku, Tokyo 105-8001, Japan; and that my citizenship and residence are as stated below next to my name:

Inventor: (Signature) _____ Date _____ Residence _____

Nobuo Nakamura

Nobuo Nakamura

Date: **JUN. 18. 1999**

Citizen of: Japan Yokohama-shi, Japan

Yoriko Tanaka

Yoriko Tanaka

Date: **JUN. 11. 1999**

Citizen of: Japan Nagoya-shi, Japan

Yoshitaka Egawa

Yoshitaka Egawa

Date: **JUN. 18. 1999**

Citizen of: Japan Yokohama-shi, Japan

Shinji Ohsawa

Shinji Ohsawa

Date: **JUN. 18. 1999**

Citizen of: Japan Ebina-shi, Japan

Tadashi Sugiki

Tadashi Sugiki

Date: **JUN. 18. 1999**

Citizen of: Japan Yokohama-shi, Japan

Yukio Endo

Yukio Endo

Date: **JUN. 18. 1999**

Citizen of: Japan Yokohama-shi, Japan

Date: _____

Citizen of: Japan _____

Date: _____

Citizen of: Japan _____